

An Approach to Polyvariant Binding Time Analysis for a Stack-Based Language

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Abstract. Binding time analysis (BTA) is used in specialization by means of partial evaluation method. Usual BTA only annotates a source program. Polyvariant BTA transforms a source program to an annotated one. Polyvariant BTA is known technique for functional languages. In this paper polyvariant BTA for a model imperative stack-based language is presented. It is described by means of building annotated control-flow graph for a source program.

1 Introduction

Partial evaluation is well known program specialization method [9]. Given values of *static* (known) arguments of a program, partial evaluation constructs a residual program — a specialized version of the source program, which on application to values of remaining *dynamic* arguments produces the same result as the source program applied to values of all arguments.

Offline partial evaluation stages the specialization in two phases: *binding time analysis* (BT-analysis, BTA) and residual program generating. BTA starts with a source program and a *binding time values* (BT-values) of all arguments and produces an annotated program.

There are two kinds of BT-analysis: *monovariant* and *polyvariant*. A monovariant BTA annotates the source program, does not transform it, whereas a polyvariant BTA generates a new annotated program. Monovariant BTAs are simple and efficient to implement [1,2,9,15,16]. Polyvariant BTAs [3,5,7,17] are more complex, but performs better result in many situations, when the same methods or variables are used in different contexts.

This paper consists of two part. First syntax and operational semantics of model imperative stack-based languages (SIL) are described. Then the polyvariant BTA for this language is presented by means of building annotated control-flow graph for a source program.

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Grammar

$$\begin{aligned}
p &\in \text{Program} & ::= & \text{instr}^* \\
\text{instr} \in \text{Instruction} & ::= & \text{Pop} \mid \text{Dup} \mid \text{Swap} \mid \text{Const}(c) \mid \text{Goto}(n) \mid \text{IfGoto}(n) \\
& & & \text{Unary}(op) \mid \text{Binary}(op) \mid \text{LoadVar}(n) \mid \text{StoreVar}(n)
\end{aligned}$$
Fig. 1. Abstract syntax of SIL-programs

2 Imperative Stack-Based Language

For describing the polyvariant BTA the imperative stack-based language (SIL) is used. SIL is a very simple stack language (fig. 1). A program at this language is just sequence of instructions (no methods and invoke instructions) with conditional and unconditional goto instructions (**IfGoto**(*m*) and **Goto**(*m*)). Other instructions are load (**LoadVar**(*n*)) and store (**StoreVar**(*n*)) data from stack to local variables, operations with data on stack (**Unary**(*op*) and **Binary**(*op*)) and simple stack operations (**Pop**, **Dup**, **Swap**).

Instructions

$$\begin{aligned}
\text{Pop} \vdash_{int} (x : st, \sigma) &\rightarrow (st, \sigma) & \text{Dup} \vdash_{int} (x : st, \sigma) &\rightarrow (x : x : st, \sigma) \\
\text{Swap} \vdash_{int} (x_1 : x_2 : st, \sigma) &\rightarrow (x_2 : x_1 : st, \sigma) & \text{Const}(c) \vdash_{int} (st, \sigma) &\rightarrow (c : st, \sigma) \\
\text{Unary}(op) \vdash_{int} (x : st, \sigma) &\rightarrow (op(x) : st, \sigma) \\
\text{Binary}(op) \vdash_{int} (x_1 : x_2 : st, \sigma) &\rightarrow (op(x_1, x_2) : st, \sigma) \\
\text{LoadVar}(n) \vdash_{int} (st, \sigma) &\rightarrow (\sigma(n) : st, \sigma) \\
\text{StoreVar}(n) \vdash_{int} (x : st, \sigma) &\rightarrow (st, \sigma[n \mapsto x])
\end{aligned}$$
Instructions with control point

$$\begin{aligned}
\text{Goto}(n) \vdash_{int} (m, (st, \sigma)) &\rightarrow (n, (st, \sigma)) \\
\text{IfGoto}(n) \vdash_{int} (m, (0 : st, \sigma)) &\rightarrow (m + 1, (st, \sigma)) \\
\text{IfGoto}(n) \vdash_{int} (m, (1 : st, \sigma)) &\rightarrow (n, (st, \sigma)) \\
\hline
&\frac{\text{instr} \vdash_{int} (st, \sigma) \rightarrow (st', \sigma')}{\text{instr} \vdash_{int} (m, (st, \sigma)) \rightarrow (m + 1, (st', \sigma'))}
\end{aligned}$$
Program

$$\begin{aligned}
&\frac{p(m) \vdash_{int} (m, (st, \sigma)) \rightarrow (m', (st', \sigma'))}{p \vdash_{int} (m, (st, \sigma)) \rightarrow (m', (st', \sigma'))} \\
&\frac{p \vdash_{int} (0, (st, \sigma_{init})) \rightarrow^* (\text{length}(p), (st', \sigma))}{p \vdash_{int} st \Rightarrow st'}
\end{aligned}$$
Fig. 2. Operational semantics of SIL-programs

The data in SIL is integer numbers. Nevertheless it is easy to extend data by other data (double numbers or boolean values). Operation op in $\mathbf{Unary}(op)$ and $\mathbf{Binary}(op)$ can be any operation with integer numbers, including, but not limited to, addition (+), subtraction (-), multiplication (\times), compare ($<$, \leq , $>$, \geq), negate and etc.

The semantic of SIL is straightforward and it is described at fig. 2. A SIL-program is evaluated by steps. Each step changes a state. Each state $(m, (st, \sigma))$ has three parts: m — number of current instruction (control point), st — stack of values, σ — mapping from local variables to their values.

A computation of a program begins from initial state $(0, (st, \sigma_{init}))$, where st — program arguments, and σ_{init} — mapping from local variables to initial value 0. At each step state is changed in according to the rules. When number of current instruction becomes equal to length of the program then evaluation of this program is finished. Values at a stack are results of this program. If number of current instruction becomes more than length of the program or no rules can be applied then evaluation of this program is terminated with error.

The SIL is similar to stack-based languages described in [2] or [15]. It contains same instruction set except array instructions and method invoke instructions.

3 Binding Time Analysis

The goal of BTA is to divide all instructions in two classes: static (**S**) and dynamic (**D**). Static instructions will be evaluated during residual program generating, dynamic instructions will be put to residual program.

The presented method of building annotated program is close to the Supercompilation [18]. It uses driving and whistling for building possibly infinity binding time tree (BT-tree) and for reducing it to finite binding time graph (BT-graph) respectively.

3.1 Driving

BT-tree is a tree with annotated instructions at nodes and with binding time states (BT-states) at ridges (fig. 4). BT-tree is similar to control-flow graph without ridge to previous nodes, each ridge is going to a new node. This BT-tree can be applied to arguments values like usual program. The BT-tree is fully equivalent to the source program: on application to values it produces the same result as the source program applied to same values.

BT-state is a state with binding time values (BT-values) **S** and **D** instead of usual values. Bold style for binding time values and variables are used below: **S** and **D** are BT-values, **x** is a binding time variable (BT-variable) that ranges over BT-values **S** and **D**.

Building of BTA tree begins with a initial BT-state $(0, (st, \sigma_{init}))$, where st — BT-values of arguments (**S** corresponds to known arguments and **D** corresponds to unknown during specialization arguments) and σ_{init} — mapping from local variables to initial BT-value **S**.

Lifting instruction

$$\mathbf{Lifting}(n) \vdash_{int} (st, \sigma) \rightarrow (st, \sigma)$$

Instructions

$$\begin{aligned} \text{Pop} &\vdash_{bta} (\mathbf{x} : st, \sigma) \rightarrow \langle \text{Pop}^{\mathbf{x}}; (st, \sigma) \rangle \\ \text{Dup} &\vdash_{bta} (\mathbf{x} : st, \sigma) \rightarrow \langle \text{Dup}^{\mathbf{x}}; (\mathbf{x} : \mathbf{x} : st, \sigma) \rangle \\ \text{Swap} &\vdash_{bta} (\mathbf{x} : \mathbf{x} : st, \sigma) \rightarrow \langle \text{Swap}^{\mathbf{x}}; (\mathbf{x} : \mathbf{x} : st, \sigma) \rangle \\ \text{Swap} &\vdash_{bta} (\mathbf{S} : \mathbf{D} : st, \sigma) \rightarrow \langle \text{Swap}^{\mathbf{S}}; (\mathbf{D} : \mathbf{S} : st, \sigma) \rangle \\ \text{Swap} &\vdash_{bta} (\mathbf{D} : \mathbf{S} : st, \sigma) \rightarrow \langle \text{Swap}^{\mathbf{S}}; (\mathbf{S} : \mathbf{D} : st, \sigma) \rangle \\ \text{Const}(c) &\vdash_{bta} (st, \sigma) \rightarrow \langle \text{Const}(c)^{\mathbf{S}}; (\mathbf{S} : st, \sigma) \rangle \\ \text{Unary}(op) &\vdash_{bta} (\mathbf{x} : st, \sigma) \rightarrow \langle \text{Unary}(op)^{\mathbf{x}}; (\mathbf{x} : st, \sigma) \rangle \\ \text{Binary}(op) &\vdash_{bta} (\mathbf{x} : \mathbf{x} : st, \sigma) \rightarrow \langle \text{Binary}(op)^{\mathbf{x}}; (\mathbf{x} : st, \sigma) \rangle \\ \text{Binary}(op) &\vdash_{bta} (\mathbf{S} : \mathbf{D} : st, \sigma) \rightarrow \langle \text{Lifting}(0)^{\mathbf{D}}, \text{Binary}(op)^{\mathbf{D}}; (\mathbf{D} : st, \sigma) \rangle \\ \text{Binary}(op) &\vdash_{bta} (\mathbf{D} : \mathbf{S} : st, \sigma) \rightarrow \langle \text{Lifting}(1)^{\mathbf{D}}, \text{Binary}(op)^{\mathbf{D}}; (\mathbf{D} : st, \sigma) \rangle \\ \text{LoadVar}(n) &\vdash_{bta} (st, \sigma) \rightarrow \langle \text{LoadVar}(n)^{\sigma(n)}; (\sigma(n) : st, \sigma) \rangle \\ \text{StoreVar}(n) &\vdash_{bta} (\mathbf{x} : st, \sigma) \rightarrow \langle \text{StoreVar}(n)^{\mathbf{x}}; (st, \sigma[n \mapsto \mathbf{x}]) \rangle \end{aligned}$$

Instructions with control point

$$\text{Goto}(n) \vdash_{bta} (m, (st, \sigma)) \rightarrow \langle \text{Goto}(n)^{\mathbf{S}}; (n, (st, \sigma)) \rangle$$

$$\text{IfGoto}(n) \vdash_{bta} (m, (\mathbf{x} : st, \sigma)) \rightarrow \langle \text{IfGoto}(n)^{\mathbf{x}}; (n, (st, \sigma)), (m + 1, (st, \sigma)) \rangle$$

$$\frac{\text{instr} \vdash_{bta} (st, \sigma) \rightarrow \langle \text{instrs}; (st', \sigma') \rangle}{\text{instr} \vdash_{bta} (m, (st, \sigma)) \rightarrow \langle \text{instrs}; (m + 1, (st', \sigma')) \rangle}$$

Program

$$\frac{p(m) \vdash_{bta} (m, (st, \sigma)) \rightarrow \langle \text{instrs}; \text{brs} \rangle}{p \vdash_{bta} (m, (st, \sigma)) \rightarrow \langle \text{instrs}; \text{brs} \rangle}$$

Fig. 3. Trace semantics for binding time trees

For each BT-state one of rules (fig. 3) is applied. The rule shows an annotated instruction at new node and one or two new BT-states at ridges in subject to current BT-state and instruction. For example, if there are BT-state $(m, (\mathbf{x} : st, \sigma))$ and current instruction $\text{IfGoto}(n)$, then it is needed to add new node with two new ridges to BT-tree. The node must contain annotated instruction $\text{IfGoto}(n)^{\mathbf{x}}$ and the ridges must contain BT-states $(n, (st, \sigma))$ and $(m + 1, (st, \sigma))$.

In some cases new instruction $\text{Lifting}(n)$ is added to a BT-tree. This instruction tells residual program generator that static (known) value in stack at depth n must be residualized by means of generating $\text{Const}(c)$ instruction and some stack instructions. For interpretation $\text{Lifting}(n)$ instruction means no operation.

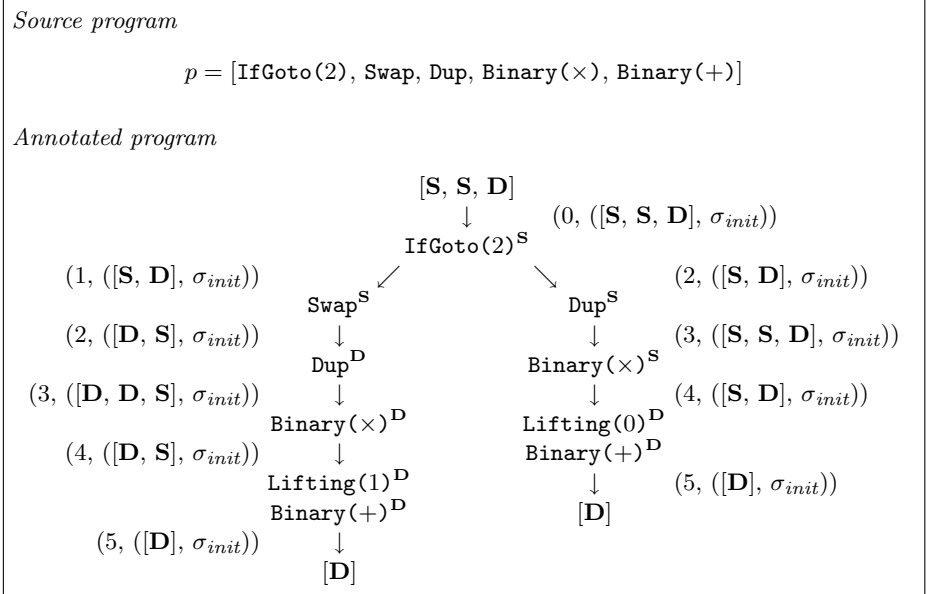


Fig. 4. BT-tree for the program $p(x, y, z) = \text{if } x \text{ then } y^2 + z \text{ else } z^2 + y$; x and y are static (known), z is dynamic (unknown).

3.2 Whistling

During BT-tree building all BT-states are checked for conjunction. If two BT-states (may be at different branches) are equal (whistling) when nodes at the end of this ridges are merged into the new node. It is permitted because BT-tree constructed from some BT-state depends on this BT-state only.

Building of BT-graph is ending because there are only finite numbers of all possible BT-states for a SIL-program. Residual BT-graph is graph representation of annotated program. Residual Program Generating for such annotated program is identical to [2].

3.3 Example

Let's consider a small program p (fig. 4). The polyvariant BTA produces a BT-graph represented at fig. 4.

This BT-graph contains two instructions $\text{Binary}(\times)$ with different BT-annotations according to different BT-annotations of stack at the same program point. At the left hand side instruction $\text{Binary}(\times)$ annotated as dynamic (\mathbf{D}), while at the right hand side instruction $\text{Binary}(\times)$ annotated as static (\mathbf{S}).

This means that if first argument of p is false during specialization then instruction $\text{Binary}(\times)$ will be residualized. In other case instruction $\text{Binary}(\times)$ will be evaluated during residual program generating.

4 Related Work

In many prior works monovariant BTAs [9] for functional languages are described. In [3,7,17] polyvariant BTAs for functional languages are presented. In minority works [1,2,15,16] monovariant or polyvariant BTAs for imperative languages are considered [13].

L. O. Andersen [1] uses C language. P. Bertelsen [2] and H. Masuhara and A. Yonezawa [15] describe monovariant BTA for various subsets of stack-based Java Byte Code [8]. In both papers simple stack-based language like SIL (which is described in this paper) is used: in [2] SIL is extended with array instruction and in [15] SIL is extended with method invocation instruction.

U. P. Schultz [16] introduces monovariant BTA some subset of Java language [8]: object-oriented but not stack-based language. Also he suggests some polyvariant (class polyvariant and method polyvariant) extensions of BTA. N. H. Christensen and R. Glück [5] present polyvariant BTA for flowchart imperative language.

Presented BTA extends prior monovariant BTAs for stack-based language by introducing control-point polyvariant, stack polyvariant and environment polyvariant annotation method. It uses infinite control-flow tree for building finite annotated graph. This new method bases on ideas of Supercompilation [18]. It is possible to enhance this method for a object-oriented stack-based language [10,11,12].

5 Conclusion

In this paper polyvariant BTA for simple stack-based language is introduced. This method is fully automatic and it is used in specializer CILPE [4,14].

In some cases polyvariant BTA can produce a huge residual program. I would like to investigate extensions of polyvariant BTA for producing a program of reasonable size. Another direction of research is to enhance this method for object-oriented stack-based languages such as Java Byte Code (Java platform) [8] and Common Intermediate Language (Microsoft .NET platform) [6] which are used in popular virtual machines.

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